<u>Amendments to the Specification</u>

Please replace the paragraph starting on p. 1, line 4 with the following rewritten paragraph:

This application is related to Application No. 09/754,903, filed January 4, 2001, attorney docket number M-8502 US, entitled "~~ISOLATION OF I2C BUSES IN A MULTIPLE POWER DOMAIN ENVIRONMENT USING MUTLIPLEXERS~~ A DISK ENCLOSURE WITH MULTIPLEXERS FOR CONNECTING I2C BUSES IN MULTIPLE POWER DOMAINS,"[[,]] and Application No. 09/755,254, filed January 4, 2001, attorney docket number M-8504 US, entitled "ISOLATION OF I2C BUSES IN A MULTIPLE POWER DOMAIN ENVIRONMENT USING SWITCHES,"[[,]] which are commonly owned and incorporated by reference herein in their entirety.

Please replace the paragraph starting on p. 4, line 6 with the following rewritten paragraph:

FIG. 1 is a block diagram illustrating the layout of a disk enclosure 102, according to ~~an embodiments~~ one embodiment of the present invention. As depicted, enclosure 102 includes eight disk sled boards 103 (only one of which is provided with a reference numeral for clarity). These disk sled boards 103 are separately labeled as DSB1 to DSB8. Each disk sled board 103 includes four disk drives 105 (only one of which is provided with a reference numeral for clarity). These disk drives are separately labeled as DD1-DD32. DD1 to DD4 are on DSB1, DD5 to DD8 are on DSB2, . .., and DD29 to DD32 are on DSB8. Each disk sled board includes other devices including backplane controllers, port bypass circuits, temperature sensors, and memory devices (shown and described later in reference to FIG. 4). It should be understood, of course, that the number of disk sled boards 103 and disk drives 105 can be varied.

Please replace the paragraph starting on p. 4, line 18 with the following rewritten paragraph:

A number of power supplies 107 (separately labeled as power supply A0, power supply A1, power supply B0 and power supply B1) provide power for disk sled boards 103. In one embodiment, power supplies A0 and A1 may be conventionally current-shared to provide n+1 redundancy; power ~~supplier~~ supplies B0 and B1 may be conventionally current-shared to provide n+1 redundancy. Power supplies A0 and A1, along with any corresponding backup batteries (described later) may provide or implement a first power domain (power domain A). Power supplies B0 and B1, along with any corresponding back batteries (described later) may provide or implement a second power domain (power domain B).

-2-